



2811

T.J. Krutsick 10

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## Patent Application

Applicant(s): Thomas J. Krutsick

Case: 10

Serial No.: 10/061,475

Filing Date: February 1, 2002

Group: 2811

Examiner: Steven Ho Yin Loke

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lisa L. Vulpis Date: December 15, 2003

Title: Method of Fabricating Complementary  
Self-Aligned Bipolar Transistors

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AMENDMENT TRANSMITTAL

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

- (1) Amendment and Response to Office Action with enclosures.

There is no additional fee due in conjunction with the Amendment. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

Date: December 15, 2003

Wayne L. Ellenbogen  
Reg. No. 43,602  
Attorney for Applicant(s)  
Ryan, Mason & Lewis, LLP  
90 Forest Avenue  
Locust Valley, NY 11560  
(516) 759-7662



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Applicant(s): Thomas J. Krutsick

Case: 10

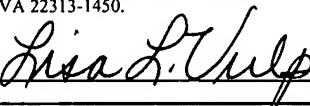
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AMENDMENT AND RESPONSE TO OFFICE ACTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated September 15, 2003, please amend the above-identified application as follows:

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